

Appl. No. 10/731,566

Dated 12/26/2006

Reply to Office Action of 08/25/2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application. No.	:	10/731,566	Confirmation No. 5068
Applicant	:	Gilbert C. Vandling	
Filed	:	12/08/2003	
Title	:	METHOD AND APPARATUS FOR TRANSFORMING SEQUENTIAL LOGIC DESIGNS INTO EQUIVALENT COMBINATIONAL LOGIC	
TC/A.U.	:	2825	
Examiner	:	SIEK, VUTHE	
Docket No.	:	1019.P003	
Customer No.	:	65068	

RESPONSE TO OFFICE ACTION

Commissioner for Patents
PO Box 1450
Alexandria VA 22313-1450

Dear Sir:

In response to the Office Action mailed on 08/25/2006 with shortened statutory period extended by petition to 12/26/2006, please enter the following amendments and remarks in the above-identified patent application as follows below:

Claims Listing begin on page 2.

Remarks begin on page 9.

Conclusion with signature is on page 11.